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	Application No.	Applicant(s)	91
Notice of Allowability	09/746,949	GILLIES ET AL.	
	Examiner	Art Unit	- <del>-</del>
	Ted T. Vo	2122	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	6 (OR REMAINS) CLOSED ) or other appropriate comn RIGHTS. This application is	in this application. If not included nunication will be mailed in due co	urse. THIS
1. X This communication is responsive to 8/24/04.			
2. ☑ The allowed claim(s) is/are <u>1,4-14,16 and 17</u> .			
3. $igotimes$ The drawings filed on <u>26 March 2001</u> are accepted by the	Examiner.		
4. Acknowledgment is made of a claim for foreign priority u a) All b) Some* c) None of the:  1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)).  * Certified copies not received:  Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.  5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which giv 6. CORRECTED DRAWINGS ( as "replacement sheets") mu (a) including changes required by the Notice of Draftsper 1) hereto or 2) to Paper No./Mail Date  (b) including changes required by the attached Examiner Paper No./Mail Date  (b) dentifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in attached Examiner's comment regarding REQUIREMENT	e been received.  e been received in Applicate ocuments have been received.  of this communication to find MENT of this application.  Initted. Note the attached Expers reason(s) why the oath was been received.  It is a submitted.  It is a submitt	ion No ed in this national stage application le a reply complying with the requi  (AMINER'S AMENDMENT or NOT or declaration is deficient.  ew ( PTO-948) attached or in the Office action of the drawings in the front (not the bater 1.121(d).  FERIAL must be submitted. Not	rements FICE OF
Attachment(s)  1. ☐ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/Paper No./Mail Date  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6.  Interview Paper No.  Examiner' 8.  Examiner' 9.  Other		
		ANTONY NGUYEN-BA PRIMARY EXAMINER	ujeu 156-

U.S. Patent and Trademark Office PTOL-37 (Rev. 1-04) Application/Control Number: 09/746,949

Art Unit: 2122

1. Responsive to the Amendment and Arguments filed on 8/24/04, filed as a Request for Continuation under 37 CFR 1.114.

Prior arts of record, Tamches et al., "Fine-grained Dynamic Instrumentation of Commodity

Operating System Kernels", anticipated to independent Claims 1, 8, and 14, remains the closest art of record. Closed art of record, Kiyohara et al., "Register Connection: A New Approach to Adding Registers into Instruction Set Architectures", is further in view to dependent Claims 4, and 6-7. Closed art of record Aho et al., "Compilers Principles, Techniques, and Tools", is further in view to dependent Claims 12-13.

Under this Amendment, independent Claims 1, 8, and 14 are amended and overcome the prior arts of record, Tamches et al.

Claims 2-3, and 15 are cancled.

## **Reasons for Allowance**

2. Claims 1, 4-14, 16-17 are allowed.

Closest art of record, Tamches, discloses a method that performs register allocation associated with a number of scratch registers. It uses a code patch, appeared as branch/jump instructions, inserted at a predetermined location in a machine code instruction sequence. Since code patch at each instrumentation point is jump or branch instructions, it requires a number of scratch registers used to perform each instrumentation point. This implies a use of certain registers for allocation. The method uses live register analysis to determine the registers that are available for scratch use at an instrumentation point. The method calculates the size of the patch used with scratch register allocation.

However, as pointed out by Applicants that the live register analysis and the assumption of a maximum number of instructions needed are different to their claim, and as further pointed out that Tamches does not teach "generating a number of registers by adding the maximum number of registers and the additional number of registers; modifying the computer executable binary to request the number of registers; and requesting the number of registers" (Remarks: page 6, fourth paragraph).

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Therefore, the following is an examiner's statement of reasons for allowance:

The cited prior arts taken alone or in combination fail to teach claimed invention to method for obtaining scratch registers for use by a procedure and its computer system and computer readable medium comprising at least features:

"generating a number of registers by adding the maximum number of registers and the additional number of registers;

modifying the computer-executable binary to request the number of registers;

and

requesting the number of registers",

as recited in independent Claim 1, and in as such manners as in Independent Claims 8 and 14.

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ted T. Vo whose telephone number is (703) 308-9049. The examiner can normally be reached on 8:00AM to 5:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tuan Q. Dam can be reached on (703) 305-4552. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

After October 28, 2004, examiner can be reached at new telephone number (571) 272-3706 and the examiner's supervisor, Tuan Q. Dam can be reached on (571) 272-3694.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

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you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TTV
Patent Examiner
Art Unit 2122
October 15, 2004

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